

TRANSMITTAL FORM

Attorney Docket No.
RPS920010127US1/2280P2133
41In re the application: **Robert T. BAILIS et al.** Confirmation No: **5286**Serial No: **10/016,449**Group Art Unit: **2133**Filed: **December 10, 2001**Examiner: **Tabone Jr., John J.**For: **Method and System for Use of a Field Programmable Gate Array (FPGA) Function within an Application Specific Integrated Circuit (ASIC) to Enable Creati**

ENCLOSURES (check all that apply)

<input checked="" type="checkbox"/>	Amendment/Reply	<input type="checkbox"/>	Assignment and Recordation Cover Sheet	<input type="checkbox"/>	After Allowance Communication to Group
<input type="checkbox"/>	After Final	<input type="checkbox"/>	Part B-Issue Fee Transmittal	<input type="checkbox"/>	Notice of Appeal
<input type="checkbox"/>	Information disclosure statement	<input type="checkbox"/>	Letter to Draftsman	<input type="checkbox"/>	Appeal Brief (in triplicate)
<input type="checkbox"/>	Form 1449	<input type="checkbox"/>	Drawings	<input type="checkbox"/>	Status Letter
<input type="checkbox"/>	(X) Copies of References	<input type="checkbox"/>	Petition	<input checked="" type="checkbox"/>	Postcard
<input checked="" type="checkbox"/>	Extension of Time Request *	<input type="checkbox"/>	Fee Address Indication Form	<input type="checkbox"/>	Other Enclosures (please identify below):
<input type="checkbox"/>	Express Abandonment	<input type="checkbox"/>	Terminal Disclaimer	RECEIVED SEP 20 2004 Technology Center 2100	
<input type="checkbox"/>	Certified Copy of Priority Doc	<input type="checkbox"/>	Power of Attorney and Revocation of Prior Powers		
<input type="checkbox"/>	Response to Incomplete Appln	<input type="checkbox"/>	Change of Correspondence Address		
<input type="checkbox"/>	Response to Missing Parts	*Extension of Term: Pursuant to 37 CFR 1.136, Applicant petitions the Commissioner to extend the time for response for two month(s), from July 13, 2004 to September 14, 2004 .			
<input type="checkbox"/>	Executed Declaration by Inventor(s)				

CLAIMS

FOR	Claims Remaining After Amendment	Highest # of Claims Previously Paid For	Extra Claims	RATE	FEE
Total Claims	15	20	0	\$18.00	\$ 0.00
Independent Claims	2	3	0	\$86.00	\$ 0.00
Total Fees					\$ 0.00

METHOD OF PAYMENT

<input checked="" type="checkbox"/>	Check no. 8039 in the amount of \$420.00 is enclosed for payment of extension fees.
<input type="checkbox"/>	Charge \$ _____ to Deposit Account No. _____ (Account Holder Name) for payment of fees.
<input checked="" type="checkbox"/>	Charge any additional fees or credit any overpayment to Deposit Account No. 50-0563 (IBM Corporation).

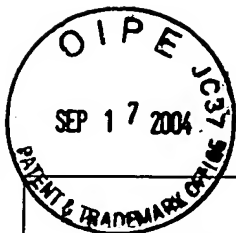
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Attorney Name	Michael R. Johnson, Reg. No. 55,306
Signature	<i>Michael R. Johnson</i>
Date	September 13, 2004

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on September 13, 2004	
Type or printed name	Jinpy Nguyen
Signature	<i>Jinpy Nguyen</i>

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Jinny Nguyen

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Date: September 13, 2004

Robert T. BAILIS et al.

Confirmation No. 5286

Serial No. 10/016,449

Group Art Unit: 2133

Filed: December 10, 2001

Examiner: Tabone, Jr., John J.

For: METHOD AND SYSTEM FOR USE OF A FIELD PROGRAMMABLE GATE
ARRAY (FPGA) FUNCTION WITHIN AN APPLICATION SPECIFIC
INTEGRATED CIRCUIT (ASIC) TO ENABLE CREATION OF A
DEBUGGER CLIENT WITHIN THE ASIC

Mail Stop Amendment
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

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Technology Center 2100

AMENDMENT

Sir:

In response to the Office Action dated April 14, 2004, Applicant submits the
following amendments and remarks:

Amendments to the Claims are reflected in the listing of claims which begins on
page 2 of this paper.

Remarks begin on page 6 of this paper.